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From

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PATENT APPLICATION TRANSMITTAL LETTER

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By: Linda Cuccia

Printed/Typed Name: Linda Cuccia

Sir:

Transmitted herewith for filing under 37 C.F.R. 1.53(b) is a(n):

- ☒ Utility
- ☒ Original patent application,

Inventor(s): **Richard J. Blount, Jay D. Peterson**

For: **SYSTEM AND METHOD FOR DELAY EQUALIZATION OF MULTIPLE TRANSMISSION PATHS**

Enclosed are:

1. ☒ 35 pages of written description, claims and abstract.
2. ☒ 4 sheets of drawings.
3. ☒ Two (2) Combined Declarations and Power of Attorneys.
 - (a) ☒ Newly executed (original or copy)
 - (b) ☐ Copy from prior application (37 CFR 1.63(d)) (for continuation/divisional if Box 5 completed)

[Note Box 5 below]

4. ☐ Incorporation by Reference (useable if Box (b) is checked).

The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 3(b), is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

5. ☐ If a **CONTINUING APPLICATION**, check appropriate box and supply the requisite information:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application Serial No:

6. ☒ Two (2) Assignment Papers (cover sheet and document(s)) of the invention to Metawave Communications Corporation
7. ☒ A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.
8. ☐ Information Disclosure Statement and Form PTO-1449. ☐ Copies of IDS Citations.

Date August 16, 2000

9. ☐ Preliminary Amendment
10. ☒ Return Receipt Postcard (MPEP 503) *(should be specifically itemized)*
11. ☐ Certified Copy of Priority Document(s) *(if foreign priority is claimed)*
12. ☐ Other: _____


13. Utility Fee Calculation

| CLAIMS | (1) FOR | (2) NUMBER FILED | (3) NUMBER EXTRA | (4) RATE | (5) CALCULATIONS |
|--------|--|------------------|------------------|-------------------------------|------------------|
| | TOTAL CLAIMS (37 C.F.R. § 1.16(e) or (j)) | 55 | - 20 = | 35 | X \$ 9 = 315.00 |
| | INDEPENDENT CLAIMS (37 C.F.R. § 1.16(b) or (i)) | 5 | - 3 = | 2 | X \$ 39 = 78.00 |
| | MULTIPLE DEPENDENT CLAIMS (if applicable) (37 C.F.R. § 1.16(d)) | | | + | = |
| | | | | Basic Fee | 345.00 |
| | | | | Total of above Calculations = | 738.00 |
| | Reduction by 50% for filing by small entity (Note 37 C.F.R. §§ 1.9, 1.27 & 1.28) | | | | |
| | * Reissue claims in excess of 20 and over original patent | | | | |
| | ** Reissue independent claims over original patent | | | TOTAL = | \$738.00 |

Method of Fee Payment

14. ☒ A check in the amount of \$738.00 to cover the filing fee is enclosed.
15. ☒ Two (2) checks in the amount of \$40.00 each to cover the assignments recordal fee are enclosed.
16. ☐ Please charge my Deposit Account No. 06-2380 in the total amount of the filing fee and the assignment recordation fee, if any. **A duplicate of this Transmittal Letter is enclosed, if box checked.**
17. ☒ The Commissioner is hereby authorized to charge any deficiency in the enclosed fees under 37 C.F.R. § 1.16, or to charge any patent application processing fees under 37 C.F.R. § 1.17, or credit any overpayment, to Fulbright & Jaworski L.L.P. Deposit Account No. 06-2380.

Respectfully submitted,



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

- ☒ In re application of Applicant or Patentee: Richard J. Blount et al.
☐ Serial or Patent No.:
☐ Filed or Issued on:

Title: SYSTEM AND METHOD FOR DEALY EQUALIZATION OF MULTIPLE TRANSMISSION PATHS

**STATEMENT CLAIMING SMALL ENTITY STATUS
(37 CFR 1.9(f) and 1.27(b)--SMALL BUSINESS CONCERN)**

I hereby state that I am

- ☐ the owner of the small business concern identified below:
☒ an official of the small business concern empowered to act on behalf of the concern identified below:

Name of Small Business Concern Metawave Communications Corporation
Address of Small Business Concern 10735 Willows Road N.E.
Redmond, Washington 98073

I hereby state that the above identified small business concern qualifies as a small business concern, as defined in 13 CFR 121.12, and reproduced in 37 CFR 1.9(d), for purposes of paying reduced fees to the United States Patent and Trademark Office under Sections 41(a) and (b) of Title 35, United States Code, in that the number of employees of the concern, including those of its affiliates, does not exceed 500 persons. For purposes of this statement, (1) the number of employees of the business concern is the average over the previous fiscal year of the concern of the persons employed on a full-time, part-time or temporary basis during each of the pay periods of the fiscal year, and (2) concerns are affiliates of each other when either, directly or indirectly, one concern controls or has the power to control the other, or a third-party or parties controls or has the power to control both.

I hereby state that rights under contract or law have been conveyed to, and remain with, the small business concern identified above, with regard to the invention described in

- ☒ the specification filed herewith, with title as listed above.
☐ the application identified above.
☐ the patent identified above.

If the rights held by the above-identified small business concern are not exclusive, each individual, concern or organization having rights in the invention is listed below and no rights to the invention are held by any person, other than the inventor, who would not qualify as an independent inventor under 37 CFR 1.9(c), if that person made the invention, or by any concern which would not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e).

Each such person, concern or organization having any rights in the invention is listed below:

- ☒ No such person, concern, or organization exists.
☐ Each such person, concern or organization is listed below.

Name _____
Address _____

☐ INDIVIDUAL ☐ SMALL BUSINESS CONCERN ☐ NONPROFIT ORGANIZATION

I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small business entity is no longer appropriate. (37 CFR 1.28(b))

- ☒ I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

Name of Person Signing: Kathryn Surace-Smith
Title of Person if Other Than Owner: Secretary
Address of Person Signing: 10735 Willows Road N.E.
Redmond, Washington 98073

SIGNATURE: Kathryn Surace-Smith

Date: 7-26-00

SYSTEM AND METHOD FOR DELAY EQUALIZATION OF MULTIPLE TRANSMISSION PATHS

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RELATED APPLICATIONS

The present invention is related to co-pending and commonly assigned United States patent application serial number 09/092,429 entitled "System and Method for Fully Self-Contained Calibration of an Antenna Array," filed June 5, 1998, the disclosure of which is hereby incorporated herein by reference.

TECHNICAL FIELD

The present invention generally relates to wireless communication systems. In particular, the present invention relates to a delay adjustment technique for signal paths in a wireless communication system.

BACKGROUND

Wireless communications often rely upon the controlled radiation/reception of radio frequency (RF) signals. For example a wireless communication system may utilize an antenna array adapted to radiate and/or receive signals within predefined areas, such as through the use of directional antenna beams. Such antenna beams may be formed using an array of multiple antenna elements arranged in a particular pattern, such as spaced a certain wave length apart, designed to form desired antenna beam patterns based on the amplitude and/or phase of the RF signals that are provided to drive each element within the array.

In a phased array antenna system, a beam forming matrix, such as a Butler matrix, providing antenna beam interfaces for coupling with base station transceiver equipment through multiple paths and antenna element interfaces for providing RF beam component signals to the antenna array. Accordingly, the beam forming matrix includes circuitry for controlling the phase and/or amplitude of generated RF beam component signals. For the system to function predictably and/or to consistently form desired antenna beams, beam forming relies on a known distribution of phase and amplitude among the RF signals in getting to the RF feed points of the antenna array. That is, for the system to provide consistent and predictable beam forming, the beam forming matrix should provide the antenna array signals with acceptably certain phase and amplitude characteristics.

However, there is often an appreciable length of transmission line and a number of components, such as amplifiers and filters, disposed in the transmission lines between transceiver equipment and the antenna array. There are often differences in the physical and/or electrical delays of the multiple paths, e.g., individual antenna beam signal paths, coupling an antenna array to associated transceiver equipment. Accordingly, the phase of signals as provided to a beam forming matrix may experience differing amounts of delay and, thus, result in undesired phase differentials there between. Moreover, there may be various electrical and/or physical path differences within a beam forming matrix. Accordingly, the phase of signals as provided to the antenna elements of an antenna array may experience differing amounts of delay and, thus, result in undesired phase differentials there between.

Phase differences on the order of a few nanoseconds when providing beam forming for signals in the 800 MHz frequency range and up can result in significant degradation of the desired radiation pattern.

5 Signals provided to a beam forming matrix and/or to the antenna elements of an antenna array may also experience different levels of amplification and/or attenuation. However, signal amplitude differentials are generally not experienced in sufficient magnitudes to significantly affect beam forming attributes.

10 Controlling (or knowing) the signals' relative phases after propagating through the different transmission paths is difficult because the different paths affect the phase of each signal differently. These difference are attributable to the transmission paths having different associated delays. The differing delays result from the paths having different electrical lengths and non-linearities associated with high order filters with steep roll-off characteristics.

15 Often systems are phase calibrated, such as at the time of assembly or deployment, to account for known transmission path delay differences, such as those associated with physical line length differences of multiple transmission paths. However, such calibration is generally only effective for a particular operating frequency. For example, RF signals as provided to a beam forming matrix, or as provided to the individual antenna elements of an antenna array, can be phase balanced at one frequency, but generally will not remain phase balanced within acceptable limits over a wide band of different frequencies.

20 However, communication systems, such as cellular telephony systems, often perform over a relatively wide frequency range (e.g., 40-50 MHz range of the 800 to 900 MHz spectrum of an advanced mobile phone service (AMPS) system). Accordingly, a system calibrated for a particular frequency of this frequency range may experience desired beam forming with respect to signals modulated at this carrier frequency while signals modulated at
25 other frequencies of the frequency band may experience beam forming of a less desirable quality. Unfortunately, it is not practicable to re-calibrate a system for separate operating frequencies. This problem is compounded when the frequency range is further widened. For

example, if a common transmission structure were to be utilized both for providing traditional analog AMPS cellular telephony communications and digital personal communication services (PCS) communications, thus requiring the system to operate over the frequencies of both services, the range of operating frequencies would vary even further from the calibration frequency.

Accordingly, what is needed is a method and system for providing to an antenna array signals having known phase relationships over multiple and different transmission paths across a desired frequency range.

SUMMARY OF THE INVENTION

These and other objects, features and technical advantages are achieved by a system and method which provides a delay equalized transmission system that allows consistent pattern formation for a relatively wide range of frequencies. According to a preferred embodiment of the present invention, delay characteristics of various relevant transmission paths are measured and delay corrected to minimize the effects of a non-linear phase response and to equalize the linear phase delay among the transmission paths.

In one embodiment, the electrical delay for each transmission path over a predetermined frequency range is determined. The delay is derived in terms of phase change versus frequency change for each path. Once the phase versus frequency function is determined, the linear component of that function is extracted so that a constant delay can be derived. Accordingly, this embodiment of the present invention operates to measure a frequency versus phase or phase versus frequency function, extract the linear component of that function, and equate that to an electrical delay.

Once the delays have been derived for each relevant path, one path is selected as a reference and the other paths are modified (e.g., electrically lengthened) so that their delays conform to that of the reference path. In this way, all of the path delays are substantially equal to one another. When the electrical delays for each path are the same and these electrical delays are linearized over a desired frequency band, then the system can operate in a broadband mode. Frequency can be changed and although the phase will be different at the different frequencies, the phase change across the different transmission paths will be substantially the same and thus phase coherency remains in tact.

According to a preferred embodiment, a step frequency or swept frequency measurement is utilized in determining signal path delay. For example, the present invention may start at one end of the frequency band, measure the phase delay experienced, increment/decrement a predetermined amount of frequency, and again measure the phase delay experienced. Preferably the entire frequency band is stepped through, although some portion thereof might be utilized, such as a statistical sample. The frequency steps of the

preferred embodiment are determined through reference to such factors as the length, physical and/or electrical, of the signal paths, the frequencies to be utilized therewith, and/or the like. Preferably, the steps are kept small enough that a phase rollover, i.e., a complete cycle, is not experienced in the phase detector. Such an embodiment is preferred because it simplifies the accumulation of the total amount of phase shift as the frequency band is stepped through.

Once the phase versus frequency function has been determined from the phase information provided by the above mentioned frequency sweep, a linear fit may be performed on that data to extract the linear component of that function. This linear component, which is the slope of the phase versus frequency function in a preferred embodiment, is utilized according to a preferred embodiment of the present invention to provide information with respect to a constant electrical delay. For example, operation according to the present invention may measure the phase versus frequency slope on all relevant signal paths, thereby determining the differences in delay associated with each such signal path, identifying a reference signal path delay, and correcting the remaining relevant signal paths such that all the slopes are the same. This being accomplished, all the delays of the relevant signal paths are the same.

According to a preferred embodiment, correction of the signal path delays is accomplished by adding additional lengths of transmission line into such signal paths. For example, one embodiment of the present invention utilizes a binary weighted delay line for each such signal path to allow selection of a desired amount of delay. Such a binary weighted delay line may be provided by a selectable set of predetermined signal line lengths, such as transmission lines of one wave length, two wave lengths, four wave lengths, etc. At a frequency of 894 MHz a wave length equates to approximately 1.1 nanosecond. Accordingly, the preferred embodiment binary weighted delay lines allow selection of various delays from any combination of delays of 1.1 nanosecond of delay, 2.2 nanoseconds of delay, 4.4 nanoseconds of delay, etc. For example, if it is determined that approximately 5 nanoseconds of delay are desired, the system may select both the 1.1 nanosecond delay and

the 4.4 nanosecond delay in series in the transmission path to provide a total of 5.5 nanoseconds of delay.

It should be appreciated that the electrical delay equalization of the present invention allows phase calibration of signal paths, such as the antenna beam signal paths coupling a phased array antenna panel to transceiver equipment, to hold over a broad range of operating frequencies. This permits formation of sector patterns that remain constant over the entire operating band. Such a technique is useful when using a multiple beam antenna system, such as those used in cellular telephony applications, where a number of signal paths, such as those of the various antenna beams, are utilized in communicating relatively low power signals in controlled areas, such as to promote frequency reuse and to improved signal quality and/or capacity. Moreover, the technique of the present invention is particularly useful with a dual mode system, such as the Spotlight® 2000 smart antenna system available from Metawave Communications Corporation, the assignee of the present application, where the frequency bands of multiple communication systems or networks, such as those of analog AMPS and digital PCS services, are communicated through common antenna structures.

The foregoing has outlined rather broadly the features and technical advantages of the present invention in order that the detailed description of the invention that follows may be better understood. Additional features and advantages of the invention will be described hereinafter which form the subject of the claims of the invention. It should be appreciated by those skilled in the art that the conception and specific embodiment disclosed may be readily utilized as a basis for modifying or designing other structures for carrying out the same purposes of the present invention. It should also be realized by those skilled in the art that such equivalent constructions do not depart from the spirit and scope of the invention as set forth in the appended claims. The novel features which are believed to be characteristic of the invention, both as to its organization and method of operation, together with further objects and advantages will be better understood from the following description when considered in connection with the accompanying figures. It is to be expressly understood,

BRIEF DESCRIPTION OF THE DRAWING

For a more complete understanding of the present invention, and the advantages thereof, reference is now made to the following descriptions taken in conjunction with the accompanying drawing, in which:

FIGURE 1 shows a block diagram of one embodiment of a delay equalization system
5 of the present invention.

FIGURE 2 shows a block diagram of one embodiment of a delay equalization module from the system of FIGURE 1.

FIGURE 3 shows one embodiment of a routine for performing a delay equalization method of the present invention.

FIGURE 4 depicts a plot of maximum frequency step versus transmission line length
10 for a delay equalized system.

FIGURE 5 graphically illustrates the detection accuracy and resolution of this method with an exemplary system.

DETAILED DESCRIPTION

A. Overview

With a preferred embodiment of the present invention, a system and method are provided for averaging the effects of non-linear phase response and equalizing the electrical delays among the transmission paths that feed a phased-array antenna. Transmission path delay equalization according to the present invention facilitates broadband phase calibration of multiple element arrays.

With a constant electrical delay, there is a linear relationship between phase change versus frequency change. Thus, delay can be derived by taking the derivative of phase as a function of frequency. Accordingly, delay can be estimated by averaging differences in phase versus differences in frequency over an acceptable frequency range and extracting a linear component from the phase versus frequency function over this range.

In one embodiment, phase calibration of a number of transmission paths is generally performed according to the following steps. Initially, information is determined regarding phase shift versus frequency for each of the relevant transmission paths, such as by utilizing relative phase detecting or phase comparator circuitry. Preferred embodiment relative phase detecting circuitry is shown and described in the above referenced patent application entitled "System and Method for Fully Self-Contained Calibration of an Antenna Array."

After determining phase shift versus frequency information, a slope of a linear fit is preferably extracted from this information, e.g., extracted from a phase versus frequency curve. The delays for each relevant path are then preferably computed from the slopes of their linear extractions. According to a most preferred embodiment, the path with the longest delay is selected as a reference path. Delay may then be added to each of the other relevant paths so that their delay is substantially equal to that of the reference path.

The addition of delays in the transmission paths according to a preferred embodiment is accomplished using a binary-controlled delay equalization circuit, such as that shown and described in co-pending and commonly assigned United States patent serial number

09/384,114 entitled "Multi-Layer Switched Line Phase Shifter," the disclosure of which is hereby incorporated herein by reference, connected in each path so that appropriate delay can be added to the non-reference paths. Of course other techniques may be utilized according to the present invention, such as selection of an appropriate individual transmission line length, the use of adjustable delay devices such as a digital signal processor (DSP), and/or the like.

With regard to the principles behind the invention, a phase-shifted waveform is displaced in time by the phase delay, or

$$T_p = \beta/\omega$$

where $\beta = \phi\pi/180$ radians and $\omega = 2\pi f$ in radians per second which results in

$$T_p = \phi/360f$$

However, absolute phase measurements across the frequency band may not be possible because the phase detector may provide a modulo 360 degree output. One way to address this phase measurement problem is to use the change in phase versus change in frequency to track phase shift as a test frequency is swept over the operating band. So long as the frequency change increments are small enough, a phase roll-over condition can be detected and properly addressed. The phase delay in terms of the slope of this tracked (or mapped) function can be expressed as,

$$T_p = 1/360(\Delta\phi/\Delta f)$$

Thus, in one embodiment of the invention, a method measures and accumulates the changes in phase versus changes in test signal frequency over the desired operating band (e.g., the A or B cellular band, a PCS band, or multiple bands where a plurality of services are accommodated) in relatively small frequency steps. Delay is then derived from the slope of a linear fit of the phase versus frequency data points (which correspond to the acquired phase versus frequency function). The linear fit (e.g., least squares method) is preferably used to average non-linear phase shifts, which are primarily due to high-order filters or filter cascades in the transmission paths.

B. Equalization System

FIGURE 1 shows one embodiment of a system for equalizing transmission paths in a communication system, such as between radio equipment (i.e., receivers, transmitters, and/or transceivers) and an antenna array. It should be appreciated that in order to present the concepts of the present invention in a more easily understandable manner, the circuit of FIGURE 1 has been simplified to illustrate only a single transmission path. However, it should be understood that it is envisioned that multiple transmission paths, such as comprising cable portion 18, preamp module 20, cable portion 12, linear power amplifier (LPA) 13, cable portion 14, and filter 15, may be utilized by the communication system and, thus, equalized according to the present invention. For example, where a multiple beam antenna system is utilized, such as shown in the above referenced patent application entitled “System and Method for Fully Self-Contained Calibration of an Antenna Array” or as shown in co-pending and commonly assigned United States patent application serial number 09/169,489 entitled “Sector Shaping Transition System and Method,” the disclosure of which is hereby incorporated herein by reference, the multiple transmission paths may be associated with various ones of the multiple antenna beams. In a preferred embodiment of the present invention, each antenna panel of a multiple antenna beam system provides four antenna beams and, thus, includes four separate paths. In turn, while only one delay equalization circuit is shown in FIGURE 1, in this embodiment there might be a separate circuit for each such antenna beam signal path.

In the embodiment of FIGURE 1, the system includes synthesis controller 11, that may include radio equipment transmitter inputs and signal path selection circuitry, such as an antenna beam signal switch matrix. Delay equalization block 21 of the present invention is included in preamp module 20 of FIGURE 1. Of course, delay equalization block 21 may be disposed elsewhere in the signal path, such as in any of the cable portions or even within synthesis controller 11, if desired.

The depicted transmission path further includes cable portion 12, LPA 13, cable portion 14, and transmit filter 15 completing the coupling of the radio equipment of synthesis

controller 11 to antenna panel coupler 16, which in turn is coupled to antenna array 16A. Antenna panel coupler 16 preferably includes coupler circuitry adapted to return a test signal sample for phase information analysis according to the present invention. For example, where a four antenna beam panel is utilized as described above, antenna panel coupler 16 may include a coupler for each antenna beam signal path and a four-way combiner that provides test signal feedback to the synthesis controller. Although the antenna array is shown separately at 16A, it should be appreciated that antenna panel coupler 16 and antenna array 16A may be embodied in a single structure. Additionally a beam forming matrix may be utilized by antenna array 16A in providing beam forming functions. This beam forming matrix may be conceptualized as a part of antenna array 16A, where delay equalization according to the present invention does not include the beam forming matrix in the signal paths equalized, or as a part of antenna panel coupler 16, where delay equalization according to the present invention does include the beam forming matrix in the signal paths equalized.

Synthesis controller 11 is preferably adapted to accept a test tone, such as from test tone generator 19, and selectively couple this test tone to transmission paths, such as through the use of the aforementioned switch matrix. Test tone generator 19 may comprise any suitable device or device combinations for providing a frequency incrementable (or decrementable) over a test signal band.

Synthesis controller 11 is also preferably adapted to accept a test tone feedback, such as from antenna panel coupler 16, and measure the phase difference between the test signal and the test signal feedback. In one embodiment, phase difference measurement is implemented with a conventional phase calibrator or comparator.

Accordingly, in operation of the preferred embodiment system of FIGURE 1 test tone generator 19 generates a first test signal at a selected frequency of a frequency band of interest, such as a lowest or highest frequency of a frequency band utilized by the communication system. Synthesis controller 11 provides this RF test signal to cable portion 18 and there through to preamp module 20. It should be appreciated that according to a preferred embodiment, delays of delay equalization block 21 are selected to present a null

delay during calibration steps. Of course, select amounts of delay may be provided by delay equalization block 21 during calibration, such as to compensate for a known path delay difference resulting in a phase rollover as between ones of the transmission paths being equalized, if desired. However, a preferred embodiment tracks and accumulates phase rollover in such signal paths, such as in a software process controlling the system, and therefore does not rely on the provision of delays during calibration.

The test signal proceeds to the linear power amplifier 13 via cable portion 12. The test signal then proceeds through cable portion 14 to the transmit filter 15, where it proceeds to antenna array 16A through the antenna panel coupler 16. Couplers within antenna panel coupler 16 return a sample of the test signal back to synthesis controller 11. Synthesis controller 11, or other suitable circuitry, measures the phase difference between the signal that it provides and the signal that it receives from the antenna panel. Test tone generator 19 generates a second test signal at a selected frequency offset from the first test signal to be routed through the transmission system as described above. When test signals of a desired frequency band have been passed through the transmission paths and measured as described above, the present invention operates to generate a phase versus frequency function for this path.

In equalizing delays of multiple transmission paths, the preferred embodiment of the present invention operates to select each such signal path and to provide the test signal thereto as described above. Selection of the signal paths may be accomplished through manipulation of a switch matrix such as may be included in synthesis circuit 11 of the preferred embodiment. Of course, other techniques may be utilized for selecting a transmission path for coupling a test signal to, such as through the use of controllable attenuation devices coupling the test signal to multiple signal paths or the activation of individual test tone generators coupled to each such signal path.

Once the system of FIGURE 1 has generated a phase versus frequency function, or like information providing a representation of the phase and frequency relationship of the transmission path, a linear representation of the phase and frequency relationship of the

transmission path is derived for use in determining an electrical delay for this path. When an electrical delay for each such transmission path has been determined, such information may be compared for a determination of an adjustment suitable at ones of the transmission paths to equalize the delays. For example, according to a most preferred embodiment, an electrical delay of greatest magnitude is selected as a reference electrical delay and a determination is made as to an amount of electrical delay to be provided by delay equalization block 21 of the remaining transmission paths to provide delay equalization. Manipulation of delay equalization block 21 may be provided by signals provided by the circuitry of synthesization circuit 11 in response to the above determination.

It should be appreciated from the above discussion of the operation of the present invention that the preferred embodiment synthesis circuit 11 includes control circuitry, such as may be provided by a processor-based system having memory associated therewith and operating under control of an instruction set of the present invention. Of course, control circuitry may be provided external to synthesis circuit 11 to provide all or a portion of the above mentioned control functions, if desired. Such control circuitry may comprise a general purpose computer system operable under control of an instruction set of the present invention and/or a special purpose computer operable under control of an instruction set of the present invention.

FIGURE 2 shows one embodiment of a preamp module 20. Preamplifier module 20 of this preferred embodiment includes delay equalization circuit 21, CDMA gain control element 200, analog gain control element 250, amplifiers 201-204, 207, 213, 214, bandpass filter 215, analog phase controller 24, RF combiner 206, memory 23, alarms 211, 212, DC-DC converter 216, reset circuit 217, and RF switch 208. In the depicted embodiment, the delay equalization circuit is implemented with a programmable gate array ("FPGA") device 21-1 and a delay adjust circuit 21-2. The CDMA gain control element 200 includes gain calibration device 202, thermal compensator 203, amplifier 204, and digital gain control 205. The analog gain control element 250 includes thermal compensator 251, digital gain controls 252 and 254 and amplifier 253.

Preamp module 20 has several inputs and outputs. The RF transmit signal (such as might be either a digital signal, such as TDMA or CDMA, or an analog, e.g., AMPS, signal) to be coupled to the antenna array is connected at inputs A. The signals of the communication system associated with users will preferably be inactive when the synthesis controller 11 is acquiring phase/frequency data and are active when the communications system is in operation since the preferred embodiment test tone steps through the frequency band of these signals. Of course, the present invention may be operated to disable or otherwise make unavailable for use only particular channels of the communication system subject to interference by the then utilized test tone, if desired. During calibration according to the present invention the test tone is preferably provided through inputs A (depending upon which signal path is being calibrated).

The phase controller 24 of a preferred embodiment, such as may be provided by a binary weighted phase shifter, is used for controlling the phase of one signal path relative to the other three. This permits broadband beam forming. Preferably, the phase adjust increments provided by phase controller 24 are smaller than those of delay equalization block 21 to allow beam forming after delay equalization is complete. Of course similar phase control may be separately provided in the digital signal path of FIGURE 2, such as within block 200, if desired.

Delay equalization circuit 21 is preferably controlled by control signals from synthesis controller 11 via a peripheral interface at input C. Input D is preferably an address line that establishes the identification of the particular FPGA to the synthesis controller 11. The controller can query each FPGA (there is preferably one for each path) and determine this address.

Block 208 of FIGURE 2 is an RF switch that is provided to control a test tone that is used for analog path diagnostics and gain control calibration. The test tone associated with block 208, therefore, is not used for the delay equalization of the present invention.

C. Equalization Method

FIGURE 3 shows a routine for performing one embodiment of a delay equalization according to the present invention. For example, it could be performed in synthesis controller 11 with the system of FIGURES 1 and 2. This preferred embodiment routine assumes that three conditions are substantially satisfied.

The first condition is that the design of the phase detector (e.g., within synthesis controller 11) for measuring the phase differences of the test signal increments is such that increasing the test frequency produces a counter-clockwise phase rotation of the test signal.

The second condition is that frequency increment size is limited to reduce phase roll-over due to frequency step. This condition may rely upon a small difference in frequency to ensure that phase difference will be less than 360 degrees during a frequency step. Preferably the maximum step size is set by the longest expected transmission path. Substituting the equation for phase delay given above into the equation $L = T_p Kc$, where L is the length of the transmission path, K is the velocity factor, and c is the speed of light, gives $L = Kc/360((\Delta\phi/\Delta f))$. Solving for Δf and setting $\Delta\phi=360$ gives $\Delta f = 984K/L$, where $c= 984$ ft/ μ s.

The graph of FIGURE 4 plots a maximum frequency step versus transmission line length. In practice, the length is measured on a two-way round trip basis with the tallest structure being about $\frac{1}{2}$ the length shown on the graph less any fixed system delays between the test signal injection point and the transmission line. For example, in a system with a 300 foot tower, the length of the phase calibration path is approximately 600 feet. In a 600 foot path with a nominal velocity factor of .85, the max. Δf for no phase roll is 1.39 MHz.

The third condition is that phase versus frequency data can be measured across the operating bandwidth. Resolution in length is due primarily to uncertainty in phase detector measurements. It can be shown that this constraint may require a large frequency to minimize the uncertainty in measured length. For example, with an 8-bit phase detector, the uncertainty in the difference between two phase measurements would be no better than 2.8

degrees. An uncertainty of 2.8 degrees at a frequency step of 1.39 MHz translates into a 4 to 5 foot uncertainty in the length measurement. The uncertainty is reduced if the frequency span is increased. The maximum possible spacing in the B band 881 to 894 MHz. (13 MHz. spacing) limits the resolution and works out to an uncertainty in length of about 6 inches.

5 Uncertainty in length is directly related to the phase difference measurement uncertainty. For example, doubling the uncertainty to 5.6 degrees would double the length uncertainty to 1 foot, or about one wave length for a typical transmission path propagation constant of .85. Measuring phase response versus frequency across the entire band also allows one to average or otherwise smooth the non-linear phase characteristics of any filters in the path that normally occur at the band edges.

10 With these conditions in mind, the steps of the preferred embodiment of FIGURE 3 will now be described. At step 301, the hardware is initialized. At step 302, a particular antenna array is selected, such as a particular antenna array panel of a multiple panel system. At step 305, the routine confirms that it has not calibrated all of the antenna arrays. If it has calibrated all of the arrays, it is completed at step 306. Otherwise, it proceeds to step 307 and selects a transmission path, such as a particular antenna beam path of a selected multiple beam antenna panel.

15 At step 308, the method of FIGURE 3 confirms that the selected transmission path is not beyond the last path to be tested (i.e., has not tested all of the paths within the array or all of the paths to have delays equalized according to the present invention). If the selected path is not beyond the parameters of the last path to be tested, then at step 313, it selects the first test frequency. At step 314, it measures the phase of this first test signal from the output of the transmission path. At step 315, it selects the next test frequency. At step 318, it confirms that this next test frequency is not out of the desired sweep range. If not, then at step 319, it measures the output phase based on this test signal.

20 At step 320, the method of FIGURE 3 confirms that this measured phase is not less than or equal to the previously measured phase. This is done to ensure that the phase has not rolled over. If the measurement is proper (greater than previous measurement), then at step

317 it calculates the phase difference for these test signals. At step 316, the phase and frequency differences are stored in memory for generating the phase versus frequency curve. From here, the routine returns to step 315 and proceeds as previously described. However, if, at step 320, the measured phase was not properly greater than the previously measured phase, then at step 321, the rollover is corrected (e.g., augmented by 360 degrees), and the routine would proceed to step 317 and calculate the appropriate phase difference as described above.

When all of the frequencies within the test range have been measured, as determined by, step 318 confirming that this next test frequency is out of the desired sweep range, then at step 312, the routine calculates the overall phase versus frequency shift curve for this path. At step 311, the slope of the curve is derived. This preferably involves forcing (or fitting) an averaged, linear component onto the curve. This may be done with a least squares methodology. At step 310, delay for this path is calculated and stored, and at step 309, the routine increments a count for selecting the next path and proceeds to step 307 where this next path is selected.

Once the delay for the last path has been calculated, as determined by step 308 confirming that the selected transmission path is not beyond the last path to be tested, the routine preferably determines which path has the longest delay (step 304) and identifies that path as a reference path. The preferred embodiment then calculates the delay differences for the other paths based on this reference path. It then can cause delay to be added in each of these other paths in accordance with their difference from the reference path. At step 303, it increments an antenna array count and goes to step 302 where it proceeds as previously described. In this way, the transmission paths for each antenna array may be equalized. Of course, there is no limitation that all antenna arrays, or even all signal paths of any particular antenna array, be equalized according to the present invention.

FIGURE 5 graphically illustrates the detection accuracy and resolution of this method with an exemplary system. In this example, test data was taken on a single transmission path of approximately 280 feet with known delays of 7.9, 2.9, 2.0, and 1.0 nanoseconds added to

the path. Using the slope of the linear fit to the test data and the relationship that $T_d = 1/360 * \text{slope}$, the measured delay differences were found to be 7.7, 2.9, 2.3, and 1.3 nanoseconds.

Although preferred embodiments of the present invention have been described with reference to a forward link signal path, it should be appreciated that the present invention may be utilized in equalizing reverse link signal paths substantially as described above. A preferred embodiment of a system utilized in the reverse link is provided as above with the path of the test signal being reversed, i.e., the test signal introduced into the antenna panel coupler and subsequently propagating through receive circuitry such as amplifiers, filters, and the like.

It should be appreciated that although the present invention has been described with reference to a multiple beam antenna panel, there is no limitation that the present invention operate with such antenna structure. For example, the present invention may operate with the antenna beam signal paths of a conical multiple beam antenna such as shown and described in United States patent number 5,940,048, the disclosure of which is hereby incorporated herein by reference. Moreover, the present invention is operable with any number of antenna system technologies presenting various signal paths benefitting from delay adjustment according to the present invention, such as antenna systems utilizing adaptive array technology. Likewise, the present invention is operable to delay adjust multiple signal paths unrelated or otherwise not coupled to antenna structure which benefit from adjustment according to the present invention.

Although preferred embodiments have been described above with reference to the use of test frequencies for measuring delays, it should be appreciated that the present invention may utilize frequencies native to the system to provide delay information, if desired. For example, the present invention may rely upon user transmissions in channels throughout the frequency band at various times to determine delay information as described above. Thereafter, this information may be utilized in the manner described above with respect to the embodiment utilizing test frequencies.

Although the present invention and its advantages have been described in detail, it should be understood that various changes, substitutions and alterations can be made herein without departing from the spirit and scope of the invention as defined by the appended claims. Moreover, the scope of the present application is not intended to be limited to the particular embodiments of the process, machine, manufacture, composition of matter, means, methods and steps described in the specification. As one of ordinary skill in the art will readily appreciate from the disclosure of the present invention, processes, machines, manufacture, compositions of matter, means, methods, or steps, presently existing or later to be developed that perform substantially the same function or achieve substantially the same result as the corresponding embodiments described herein may be utilized according to the present invention. Accordingly, the appended claims are intended to include within their scope such processes, machines, manufacture, compositions of matter, means, methods, or steps.

WHAT IS CLAIMED IS:

1. A method of equalizing electrical delay of a plurality of signal paths, comprising:

measuring electrical delay for each of the plurality of signal paths over a selected frequency range;

5 determining a linear relationship between phase and frequency for said frequency range at least in part through reference to measured electrical delay information; and

modifying the delay of one or more paths of said plurality of signal paths based at least in part on said linear relationship to provide a substantially equal electrical delay over said selected frequency range.

2. The method of claim 1, wherein the act of modifying includes controllably adding delay to the one or more paths.

3. The method of claim 2, wherein controllably adding delay comprises:
selecting delays of a binary weighted delay line substantially providing a desired amount of delay.

4. The method of claim 1, further comprising:
selecting as a reference path a path of said plurality of signal paths with a largest delay and adding delay to the other paths of said plurality of signal paths so that their delay is substantially equal to the reference path.

5. The method of claim 1, wherein determining a linear relationship comprises:
deriving a phase versus frequency change curve for each path.

6. The method of claim 5, wherein the phase versus frequency change curve is derived by measuring change in path output phase versus change in path input test frequency, wherein a plurality of test signals having incrementally increasing frequencies are input to a path in deriving its curve.

7. The method of claim 6, wherein the plurality of incrementally increasing test frequencies have incremental increases that are determined at least in part to be sufficiently small to sufficiently reduce phase rollover.

8. The method of claim 5, wherein determining a linear relationship further comprises:

linear fitting the curve to derive an overall slope for the curve.

9. The method of claim 8, wherein the delay for each path of said plurality of signal paths is derived from the linear-fitted slope.

10. The method of claim 1, wherein said selected frequency range is a range of frequencies substantially corresponding to a range of frequencies to be communicated through said signal paths.

11. The method of claim 10, wherein said range of frequencies to be communicated through said signal paths includes a range of frequencies associated with a first communication service and a range of frequencies associated with a second communication service.

12. The method of claim 10, wherein said range of frequencies to be communicated through said signal paths is a range of frequencies associated with communication services provided by at least one of the communication service group consisting of:

- analog cellular telephony;
- CDMA cellular telephony;
- TDMA cellular telephony; and
- PCS communications.

13. A method of phase calibrating signal paths of a multiple beam communication system having a phased array antenna coupled to a beam forming matrix, wherein a plurality of antenna beam signal interfaces are provided by the beam forming matrix for coupling to a plurality of antenna beam signal paths of the communication system, the method comprising:

5 selecting a first group of antenna beam signal paths from said plurality of antenna beam signal paths for delay equalization;

selecting a test frequency range and a test frequency increment/decrement;

10 measuring delay for each signal path of the first group of antenna beam signal paths with respect to said test frequency range at intervals substantially corresponding to said increment/decrement;

determining a relationship between phase and frequency for each signal path of the first group of antenna beam signal paths at least in part through reference to measured delay information associated therewith; and

15 adjusting a delay of one or more paths of the first group of antenna beam signal paths based at least in part on said relationship between phase and frequency.

14. The method of claim 13, wherein said first group of antenna beam signal paths are associated with a same multiple beam antenna structure.

15. The method of claim 14, wherein said plurality of antenna beam signal paths include a second group of antenna beam signal paths associated with a second multiple beam antenna structure.

16. The method of claim 15, further comprising:

selecting said second group of antenna beam signal paths for delay equalization;

measuring delay for each signal path of the second group of antenna beam signal paths with respect to said test frequency range at intervals substantially corresponding to said increment/decrement;

determining a relationship between phase and frequency for each signal path of the second group of antenna beam signal paths at least in part through reference to measured delay information associated therewith; and

adjusting a delay of one or more paths of the second group of antenna beam signal paths based at least in part on said relationship between phase and frequency.

17. The method of claim 13, wherein the test frequency range is selected to substantially correspond to signals to be communicated by said communication system.

18. The method of claim 17, wherein said test frequency range is at least 40 MHz.

19. The method of claim 17, wherein said test frequency range substantially corresponds to a cellular carrier frequency assignment.

20. The method of claim 17, wherein said test frequency range substantially corresponds to frequency assignments of two communication services.

21. The method of claim 13, wherein measuring delay for each signal path of said first group of antenna beam signals comprises:

determining change in phase versus change in frequency to track phase shift as the test frequency is swept over the test frequency range.

22. The method of claim 13, wherein the test frequency increment/decrement is selected to provide increments small enough that a phase roll-over condition can be detected.

23. The method of claim 13, wherein determining a relationship between phase and frequency for each signal path of said first group of antenna beam signal paths comprises:

determining a linear relationship between phase and frequency for each signal path.

24. The method of claim 23, wherein said linear relationship comprises:

deriving a phase versus frequency change curve for each signal path of said first group of antenna beam signal paths and extracting a linear component from the phase versus frequency function.

25. The method of claim 24, wherein the delay for each signal path of said first group of antenna beam signal paths is derived from said linear component.

26. A method for measuring the delay of a transmission path having an input and an output, comprising:

generating a phase change versus frequency change curve for the path;

linearizing the generated curve to form a line; and

deriving the path's delay from the slope of the line.

27. The method of claim 26, wherein the act of linearizing the curve includes the act of averaging a plurality of curve values using a least squares methodology.

28. The method of claim 26, wherein the act of generating a phase change versus frequency change curve includes:

applying a test signal having a first frequency at the path input;

measuring the phase at the path output;

recording the phase difference between said measured phase and the first frequency,

incrementing the value of the first frequency;

repeating the previous acts until the incremented first frequency has traversed a desired range.

29. A system for adjusting electrical delay of a plurality of signal paths, comprising:

a plurality of signal paths;

phase comparison circuitry providing a comparison of a phase of an input signal with a phase of a feedback signal and providing information with respect to an electrical delay for each of the plurality of signal paths over a selected frequency range;

processor circuitry in communication with said phase comparison circuitry and operable upon said electrical delay information to determine a linear relationship between phase and frequency for each of the plurality of signal paths for said frequency range; and

phase adjustment circuitry in communication with said processor circuitry and accepting control signals therefrom to modify a delay of one or more signal paths of said plurality of signal paths based at least in part on said linear relationship.

30. The system of claim 29, wherein modification of delays by said phase adjustment circuitry provides substantially equivalent signal path electrical delays with respect to each signal path of said plurality of signal paths.

31. The system of claim 29, further comprising:

a test frequency generator controllably coupleable to said plurality of signal paths, wherein said test frequency generator provide test frequencies throughout said frequency range.

32. The system of claim 31, wherein said test frequencies provided by said test frequency generator are distributed throughout said frequency range substantially at predetermined intervals.

33. The system of claim 32, wherein said predetermined intervals determined at least in part to be sufficiently small to sufficiently reduce phase rollover.

34. The system of claim 31, wherein control of connection of said test frequency generator to a particular signal path of said plurality of signal paths is provided by a processor-based controller of said processor circuitry.

35. The system of claim 34, wherein said controller operates to couple said test frequency generator to each signal path of said plurality of signal paths separately.

36. The system of claim 35, further comprising:

a switch matrix coupled to said processor-based controller, said test frequency generator, and said plurality of signal paths, wherein said separate coupling of said test frequency generator is provided by manipulation of said switch matrix by said processor-based controller.

37. The system of claim 35, further comprising:

a first antenna structure, wherein said plurality of signal paths are coupled to said first antenna structure.

38. The system of claim 37, further comprising:

a second antenna structure, wherein another plurality of signal paths are coupled to said second antenna structure.

39. The system of claim 29, wherein said phase adjustment circuitry comprises:
a plurality of predetermined amounts of delay.

40. The system of claim 39, wherein ones of said predetermined amounts of delay
are selected under control of a processor-based system.

41. The system of claim 39, wherein said plurality of predetermined amounts of
delay are provided by a binary weighted delay line apparatus.

42. The system of claim 29, wherein said processor circuitry comprises:
a processor-based controller adapted to select a reference signal path from said
plurality of signal paths based at least in part on said electrical delay information, wherein
modification of said delay of said one or more signal paths of said plurality of signal paths is
made with reference to said reference signal path.

43. The system of claim 29, wherein said frequency range is a range of
frequencies substantially corresponding to a range of frequencies to be communicated
through said signal paths.

44. The system of claim 43, wherein said range of frequencies to be
communicated through said signal paths includes a range of frequencies associated with a
first communication service and a range of frequencies associated with a second
communication service.

45. The system of claim 43, wherein said range of frequencies to be communicated through said signal paths is a range of frequencies associated with communication services provided by at least one of the communication service group consisting of:

- 5 analog cellular telephony;
 CDMA cellular telephony;
 TDMA cellular telephony; and
 PCS communications.

46. A system for phase calibrating signal paths of a multiple beam communication system having a phased array antenna coupled to a beam forming matrix, wherein a plurality of antenna beam signal interfaces are provided by the beam forming matrix for coupling to a plurality of antenna beam signal paths of the communication system, the system comprising:

5 a first group of antenna beam signal paths;
a test frequency source providing test frequency signals in a predetermined frequency range;

10 a phase comparator coupled to said first group of antenna beam signal paths and to a feedback signal path, wherein said phase comparator is adapted to measure delay for each signal path of the first group of antenna beam signal paths with respect to said test frequency signals provided by said test frequency source;

15 a processor-based system operable to determine a relationship between phase and frequency for each signal path of the first group of antenna beam signal paths at least in part through reference to measured delay information provided by said phase comparator; and

phase adjustment circuitry disposed in ones of said first group of antenna beam signal paths providing adjustable delays in said ones of said first group of antenna beam signal paths based at least in part on said relationship between phase and frequency.

47. The system of claim 46, wherein said phase adjustment circuitry is operable to adjust delays in said ones of said first group of antenna beam signal paths under control of said processor-based system.

48. The system of claim 47, wherein said phase adjustment circuitry comprises a binary switched delay circuit.

49. The system of claim 46, wherein said phase comparator is operable under control of said processor-based system.

50. The system of claim 46, wherein said further comprising:

a multiple antenna beam structure, wherein said plurality of signal paths are antenna beam signal paths associated with antenna beams of said multiple antenna beam structure.

51. The system of claim 46, wherein the predetermined frequency range is selected to substantially correspond to signals to be communicated by said communication system.

52. The system of claim 51, wherein the predetermined frequency range substantially corresponds to a cellular carrier frequency assignment.

53. The system of claim 51, wherein the predetermined frequency range substantially corresponds to frequency assignments of two communication services.

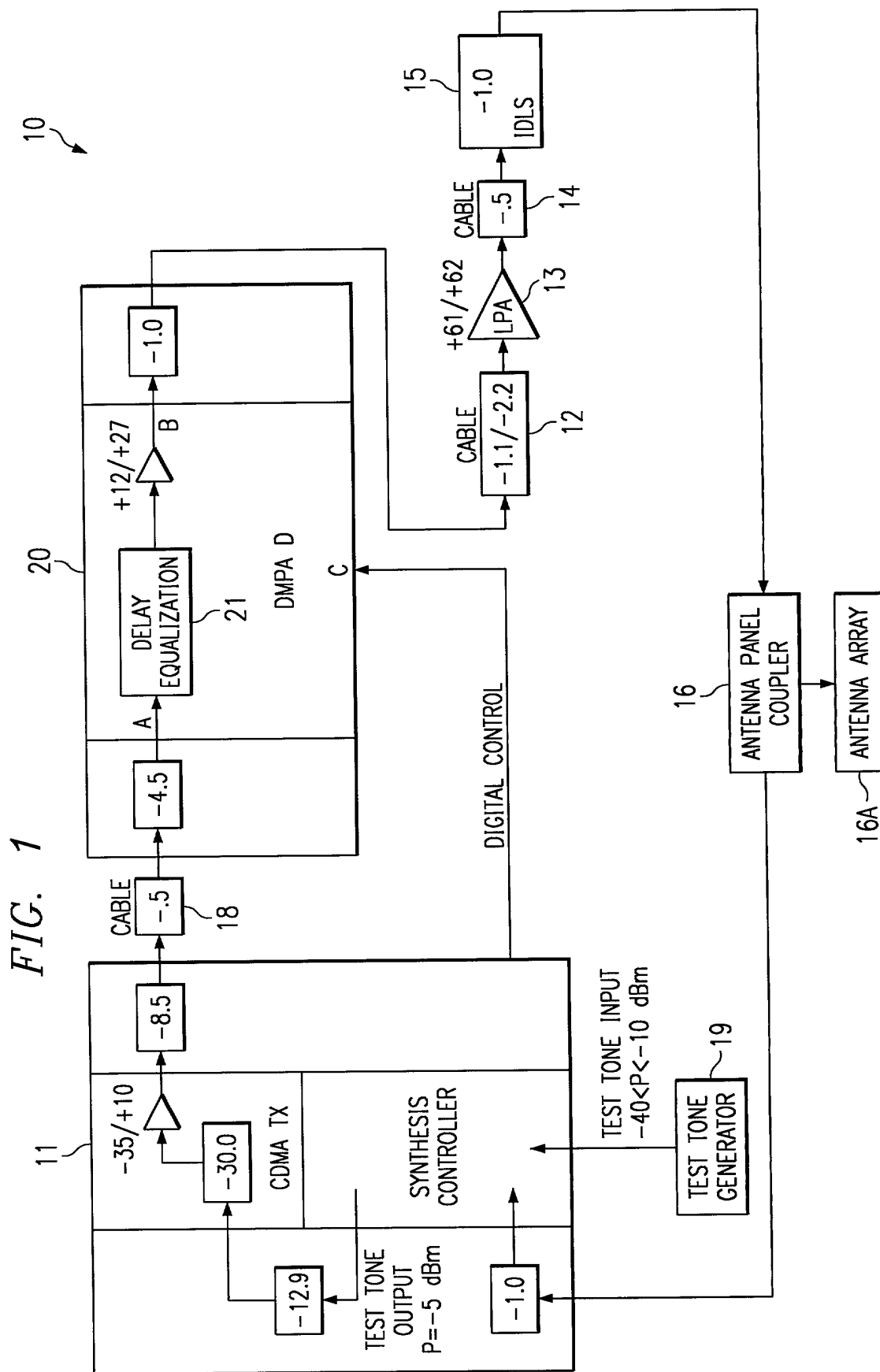
54. The system of claim 46, wherein said test frequency signal in said predetermined frequency range are distributed throughout said predetermined frequency range substantially at predetermined intervals.

55. The system of claim 54, wherein the predetermined intervals are selected to facilitate detection of a phase roll-over condition.

System and Method for Delay Equalization of Multiple Transmission Paths**ABSTRACT OF THE DISCLOSURE**

The present invention provides systems and methods for addressing the effects of non-linear phase response, such as by equalizing electrical delays among transmission paths. A preferred embodiment provides phase detection circuitry and phase adjustment circuitry in the signal paths of a multiple beam or phased array antenna to provide delay equalization of the signal paths. Communication systems implementing the systems and methods of the present invention are provided broadband phase calibration and, therefore, are enabled to provide controlled and predictable beam forming over a broad range of frequencies.

8/16/00



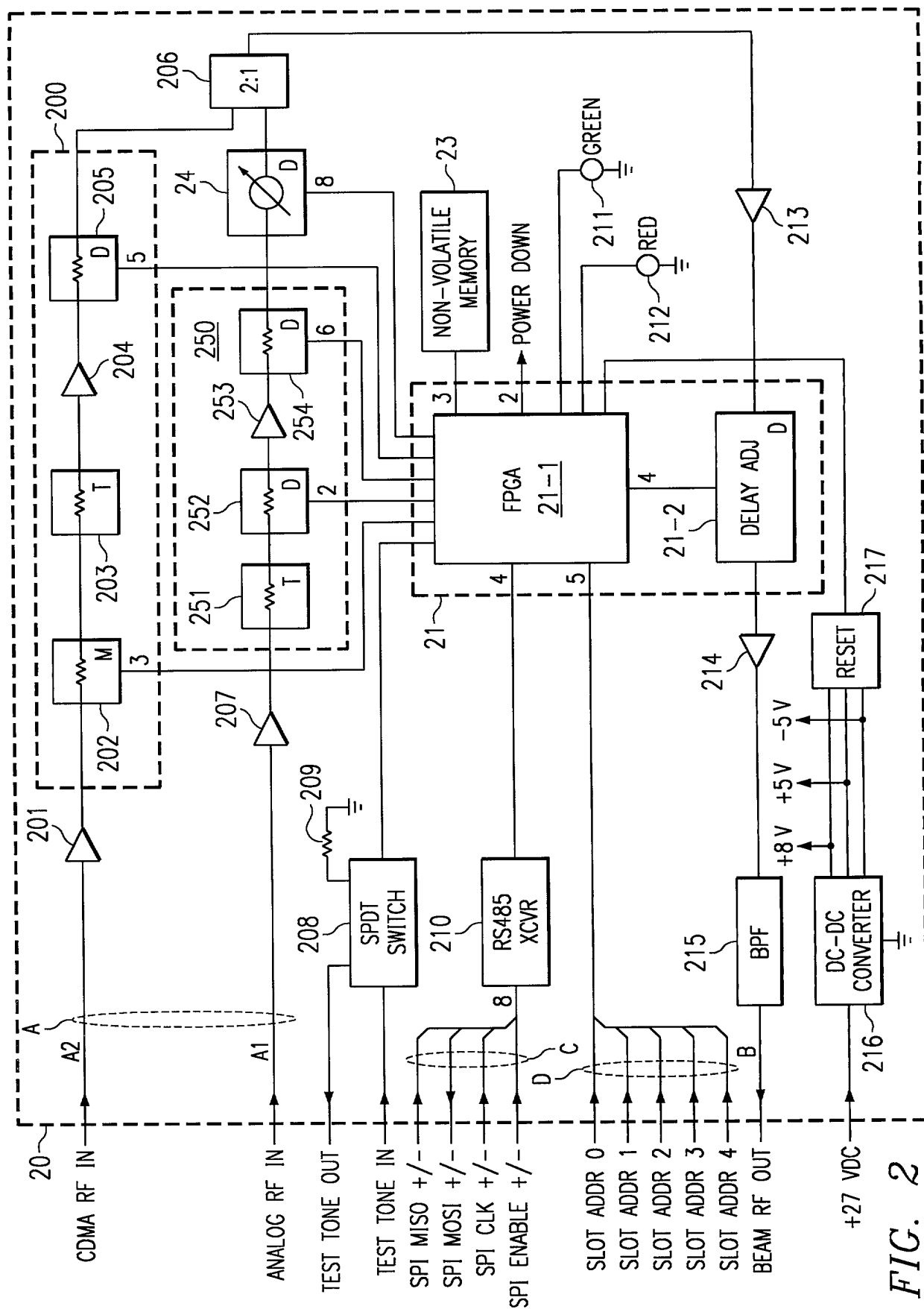
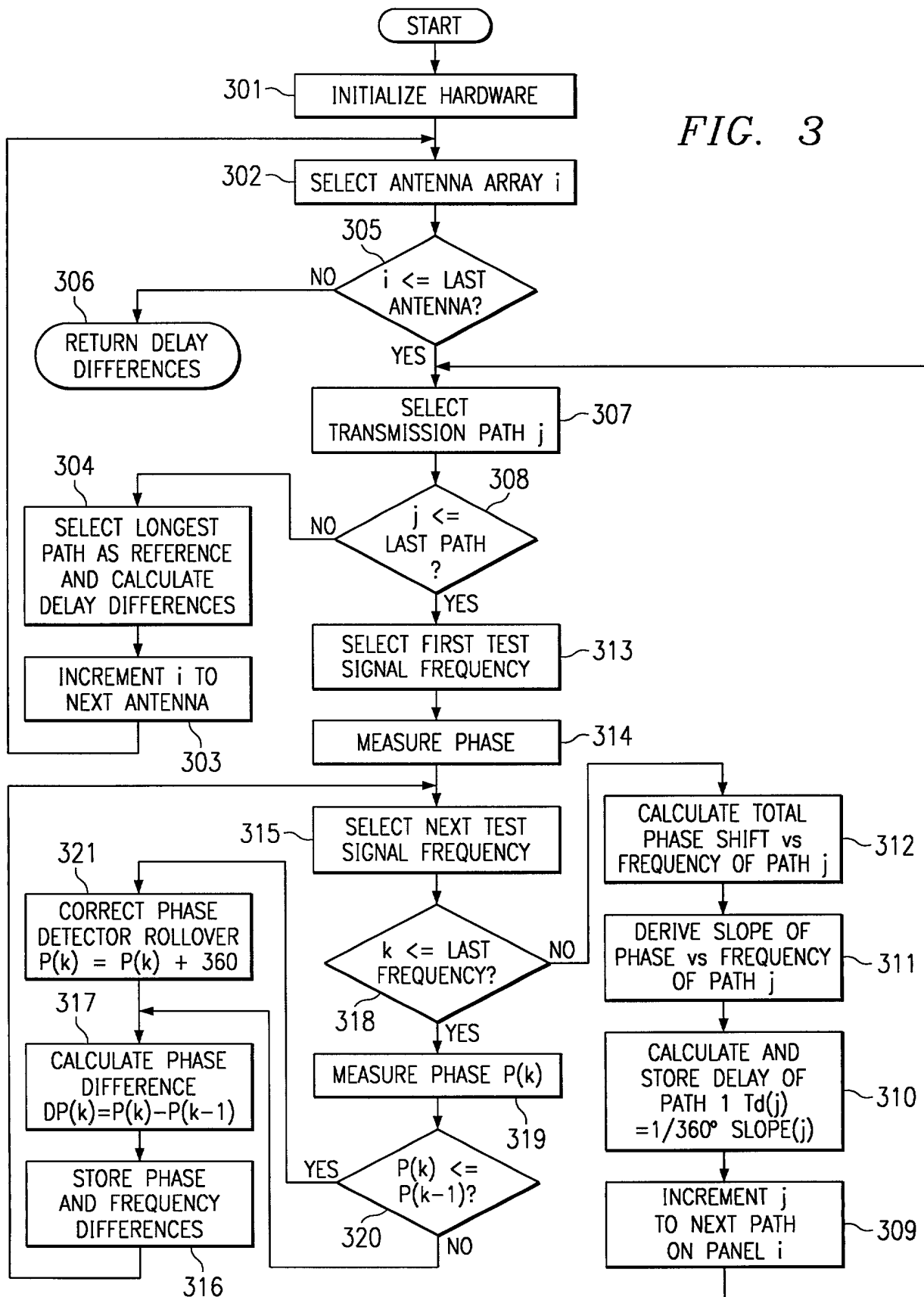


FIG. 2

FIG. 3



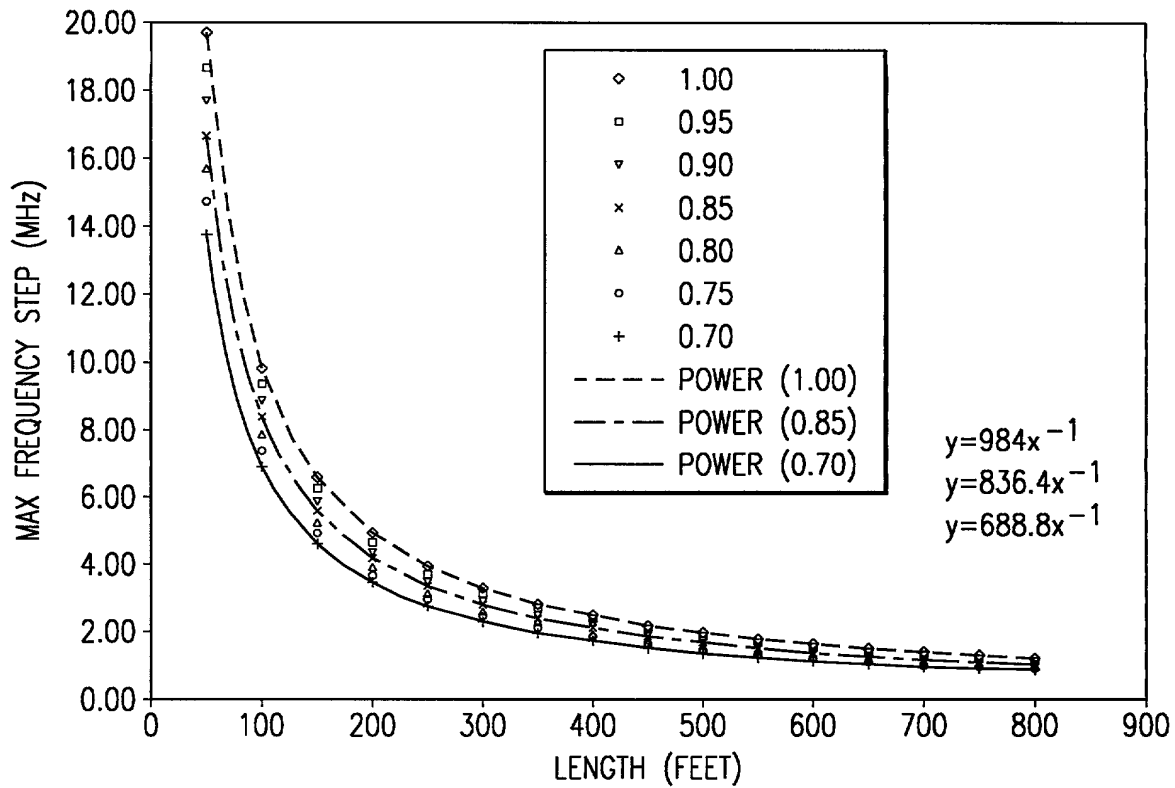


FIG. 4

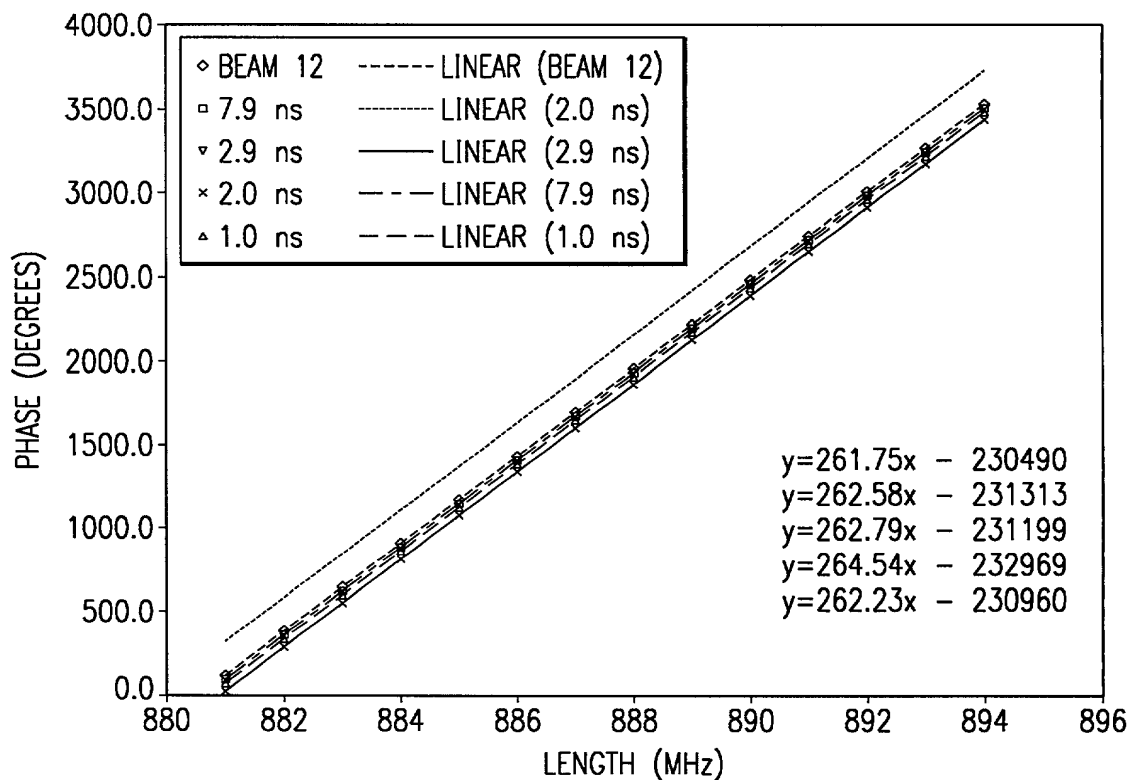


FIG. 5

COMBINED DECLARATION AND POWER OF ATTORNEY

(ORIGINAL, DESIGN, NATIONAL STAGE OF PCT, SUPPLEMENTAL, DIVISIONAL,
CONTINUATION, OR C-I-P)

As a below named inventor, I hereby declare that:

TYPE OF DECLARATION

This declaration is of the following type:

- ☒ original.
- ☐ design.
- ☐ supplemental.
- ☐ national stage of PCT.
- ☐ divisional.
- ☐ continuation.
- ☐ continuation-in-part (C-I-P).

INVENTORSHIP IDENTIFICATION

My residence, post office address and citizenship are as stated below, next to my name. I believe that I am the original, first and sole inventor (*if only one name is listed below*) or an original, first and joint inventor (*if plural names are listed below*) of the subject matter that is claimed, and for which a patent is sought on the invention entitled:

TITLE OF INVENTION

SYSTEM AND METHOD FOR DELAY EQUALIZATION OF MULTIPLE TRANSMISSION PATHS

SPECIFICATION IDENTIFICATION

The specification of which:

- (a) ☒ is attached hereto.
- (b) ☐ was filed on _____, as ☐ Serial No. 0 / _____ or
☐ _____ and was amended on _____ (*if applicable*).
- (c) ☐ was described and claimed in PCT International Application No. _____ filed on
_____ and as amended under PCT Article 19 on _____ (*if any*).

SUPPLEMENTAL DECLARATION (37 CFR 1.67(b))

- ☐ I hereby declare that the subject matter of the
- ☐ attached amendment
- ☐ amendment filed on _____.

was part of my/our invention and was invented before the filing date of the original application, above identified, for such invention.

ACKNOWLEDGMENT OF REVIEW OF PAPERS AND DUTY OF CANDOR

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in 37, Code of Federal Regulations, § 1.56,

- ☐ in compliance with this duty, there is attached an information disclosure statement, in accordance with 37 CFR 1.98.

PRIORITY CLAIM (35 U.S.C. § 119(a)-(d))

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed.

- (d) ☒ no such applications have been filed.
- (e) ☐ such applications have been filed as follows.

**PRIOR FOREIGN/PCT APPLICATION(S) FILED WITHIN 12 MONTHS
(6 MONTHS FOR DESIGN) PRIOR TO THIS APPLICATION
AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. § 119(a)-(d)**

| COUNTRY (OR INDICATE IF PCT) | APPLICATION NUMBER | DATE OF FILING DAY, MONTH, YEAR | PRIORITY CLAIMED UNDER 35 USC 119 | |
|------------------------------------|-----------------------|------------------------------------|--------------------------------------|-----------------------------|
| | | | <input type="checkbox"/> Yes | <input type="checkbox"/> No |
| | | | <input type="checkbox"/> Yes | <input type="checkbox"/> No |
| | | | <input type="checkbox"/> Yes | <input type="checkbox"/> No |

CLAIM FOR BENEFIT OF PRIOR U.S. PROVISIONAL APPLICATION(S)
(35 U.S.C. § 119(e))

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

| PROVISIONAL APPLICATION NUMBER | FILING DATE |
|--------------------------------|-------------|
| _____/_____ | _____ |
| _____/_____ | _____ |
| _____/_____ | _____ |

CLAIM FOR BENEFIT OF EARLIER U.S./PCT APPLICATION(S)
UNDER 35 U.S.C. § 120

☐ I hereby claim the benefit under Title 35, United States Code § 120 of any United States application(s) or § 365(b) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior U.S. or PCT international application in the manner provided by the first paragraph of Title 35, U.S.C. § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

| APPLICATION SERIAL | FILING DATE | STATUS |
|--------------------|-------------|--------|
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| | | |
| | | |

ALL FOREIGN APPLICATION(S), IF ANY, FILED MORE THAN 12 MONTHS
(6 MONTHS FOR DESIGN) PRIOR TO THIS U.S. APPLICATION

POWER OF ATTORNEY

I hereby appoint the following practitioner(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

David H. Tannenbaum, Reg. No. 24,745;
Michael A. Papalas, Reg. No. 40,381;
R. Ross Viguet, Reg. No. 42,203;
Michael J. Fogarty, III, Reg. No. 42,541;
Jody Bishop, Reg. No. 44,034;
Thomas J. Meaney, Reg. No. 41,990 and
Matthew Jones, Reg. No. 44,810.

SEND CORRESPONDENCE TO

DIRECT TELEPHONE CALLS TO:

David H. Tannenbaum
FULBRIGHT & JAWORSKI L.L.P.
2200 Ross Avenue, Suite 2800
Dallas, Texas 75201.

R. Ross Viguet
(214) 855-8185

DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

SIGNATURE(S)

NOTE: Carefully indicate the family (or last) name, as it should appear on the filing receipt and all other document.

Full name of sole or first inventor: Richard J. Blount

Inventor's signature : _____

Country of Citizenship: U.S.A.

Date: _____

Residence: 35116 11th Place SW, Federal Way, WA 98023

Post Office Address: 35116 11th Place SW, Federal Way, WA 98023

COMBINED DECLARATION AND POWER OF ATTORNEY

(ORIGINAL, DESIGN, NATIONAL STAGE OF PCT, SUPPLEMENTAL, DIVISIONAL,
CONTINUATION, OR C-I-P)

As a below named inventor, I hereby declare that:

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This declaration is of the following type:

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TITLE OF INVENTION**SYSTEM AND METHOD FOR DELAY EQUALIZATION OF MULTIPLE TRANSMISSION PATHS****SPECIFICATION IDENTIFICATION**

The specification of which:

- (a) ☒ is attached hereto.
- (b) ☐ was filed on _____, as ☐ Serial No. 0 / _____ or
☐ _____ and was amended on _____ (*if applicable*).
- (c) ☐ was described and claimed in PCT International Application No. _____ filed on
_____ and as amended under PCT Article 19 on _____ (*if any*).

SUPPLEMENTAL DECLARATION (37 CFR 1.67(b))

- ☐ I hereby declare that the subject matter of the
- ☐ attached amendment
- ☐ amendment filed on _____.

was part of my/our invention and was invented before the filing date of the original application, above identified, for such invention.

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- (d) ☒ no such applications have been filed.
- (e) ☐ such applications have been filed as follows.

**PRIOR FOREIGN/PCT APPLICATION(S) FILED WITHIN 12 MONTHS
(6 MONTHS FOR DESIGN) PRIOR TO THIS APPLICATION
AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. § 119(a)-(d)**

| COUNTRY (OR INDICATE IF PCT) | APPLICATION NUMBER | DATE OF FILING DAY, MONTH, YEAR | PRIORITY CLAIMED UNDER 35 USC 119 | |
|------------------------------------|-----------------------|------------------------------------|--------------------------------------|--------|
| | | | [] Yes | [] No |
| | | | [] Yes | [] No |
| | | | [] Yes | [] No |

CLAIM FOR BENEFIT OF PRIOR U.S. PROVISIONAL APPLICATION(S)
(35 U.S.C. § 119(e))

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

| PROVISIONAL APPLICATION NUMBER | FILING DATE |
|--------------------------------|-------------|
| _____/_____ | _____ |
| _____/_____ | _____ |
| _____/_____ | _____ |

CLAIM FOR BENEFIT OF EARLIER U.S./PCT APPLICATION(S)
UNDER 35 U.S.C. § 120

☐ I hereby claim the benefit under Title 35, United States Code § 120 of any United States application(s) or § 365(b) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior U.S. or PCT international application in the manner provided by the first paragraph of Title 35, U.S.C. § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

| APPLICATION SERIAL | FILING DATE | STATUS |
|--------------------|-------------|--------|
| | | |
| | | |
| | | |

ALL FOREIGN APPLICATION(S), IF ANY, FILED MORE THAN 12 MONTHS
(6 MONTHS FOR DESIGN) PRIOR TO THIS U.S. APPLICATION

POWER OF ATTORNEY

I hereby appoint the following practitioner(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

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DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

SIGNATURE(S)

NOTE: Carefully indicate the family (or last) name, as it should appear on the filing receipt and all other document.

Full name of sole or first inventor: Richard J. Blount

Inventor's signature : Richard J. Blount

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